

**Amendments to the Specification:**

Please replace paragraph the second full paragraph on page 2 with the following amended paragraph:

E2 Generally, one scan-path is provided within one computer system, and a number of registers are serially connected in this scan-path. For example, in the target system 100 in FIG. 4, 7 registers RA1, RA2, RB1, RB2, RB3~~[[\*]]~~, RC1, and RC2 are serially connected in the scan-path (1). These registers RA1, RA2, RB1, RB2, RB3, RC1, and RC2 are accessed in parallel in normal operations, and all of these are serially accessed when the scan-path is used. Both ends of this scan-path ① are connected to the emulator 110 through multiplexers 102 and 104 and data input and output terminals 106 and 108.

Please delete the notation at the bottom of page 2 as indicated herewith:

| ~~\*-Translator's note: RB3 is mislabeled in FIG. 4.~~